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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10086505	FILING DATE 02/28/2002	CLASS 257	SUBCLASS	GAU 2811	EXAMINER
**APPLICANTS: Geruganthu, Rama Bruce Michael					
**CONTINUING DATA VERIFIED					
** FOREIGN APPLICATIONS VERIFIED:					
FIG PUB <input type="checkbox"/> DO NOT PUBLISH		RESCIND <input type="checkbox"/>		ATTORNEY DOCKET NO AMDA 521PA	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no Foreign priority conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners initials					
TITLE : Circuit analysis using electric field induced effects					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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